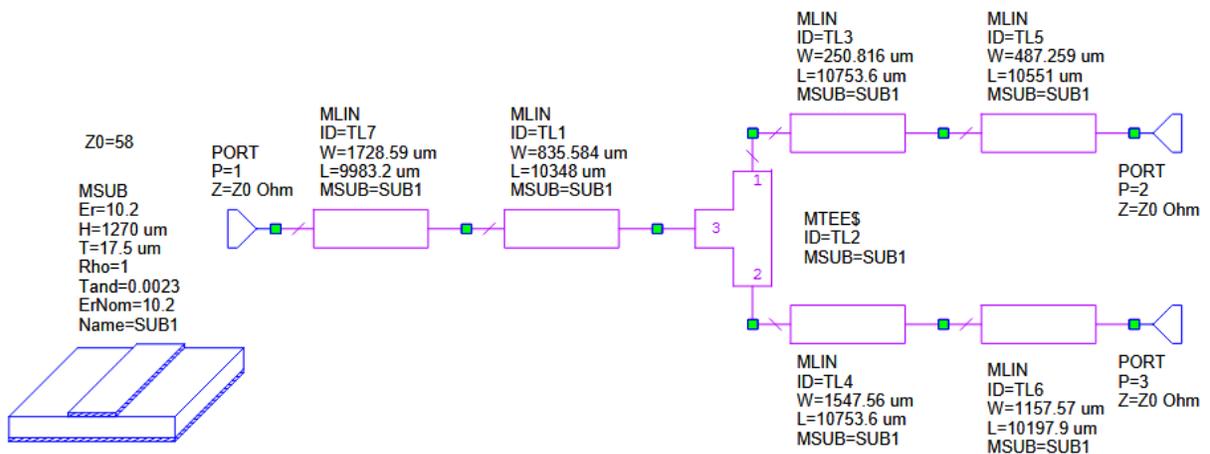


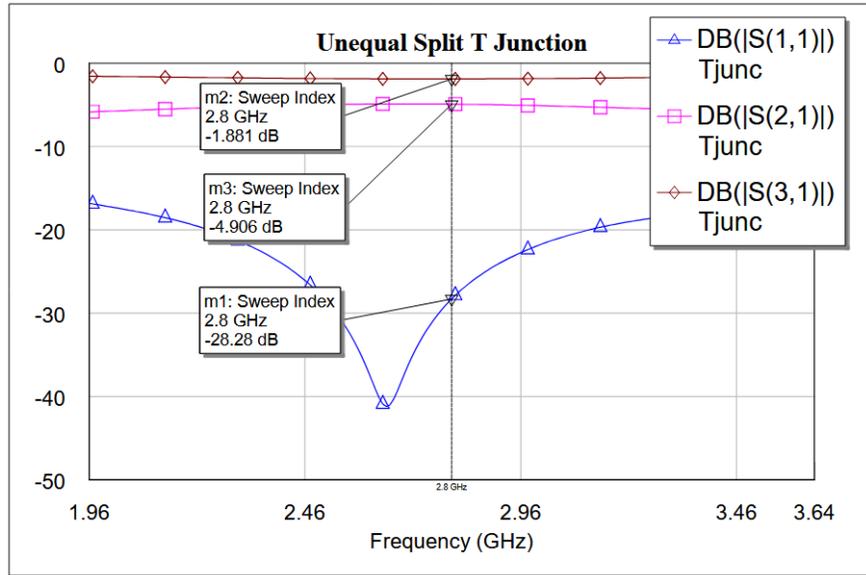
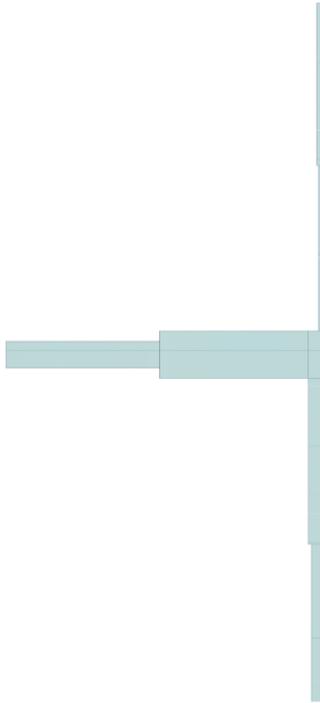
Test 2 – Simulation Part

My specifications were a Z_0 of 58Ω and a f_0 of 2.8 GHz.

1:2 T-Junction Power Divider:

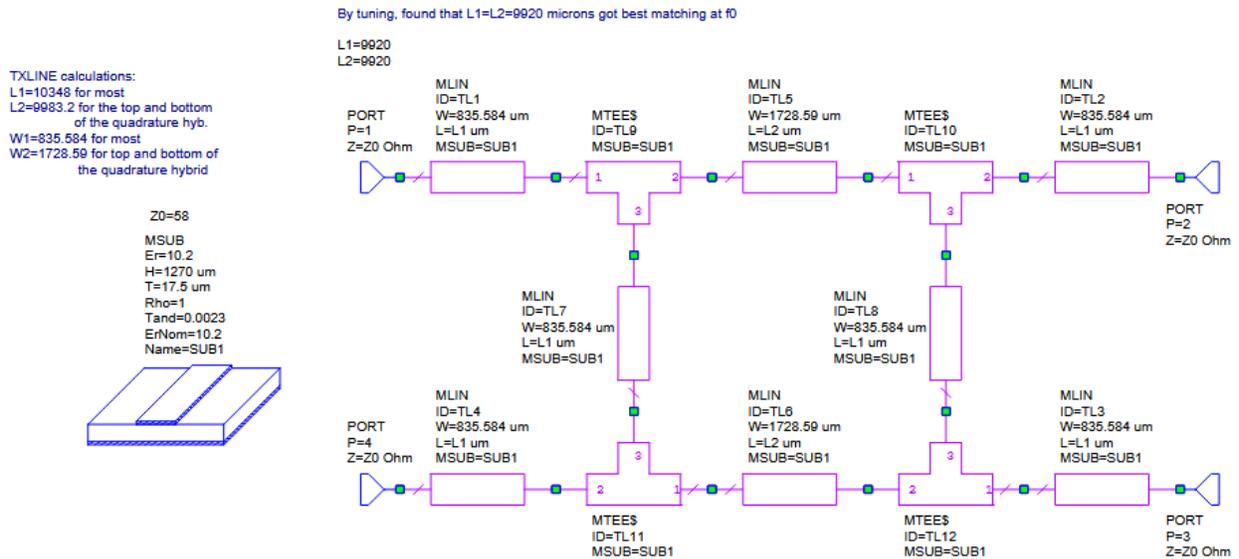
The Z_0 I was assigned causes one of the outputs to necessitate a 174Ω line, which is not viable for microstripline. Therefore, my Z_0 for the power divider was 29Ω and I added quarter-wave transformers at each port.

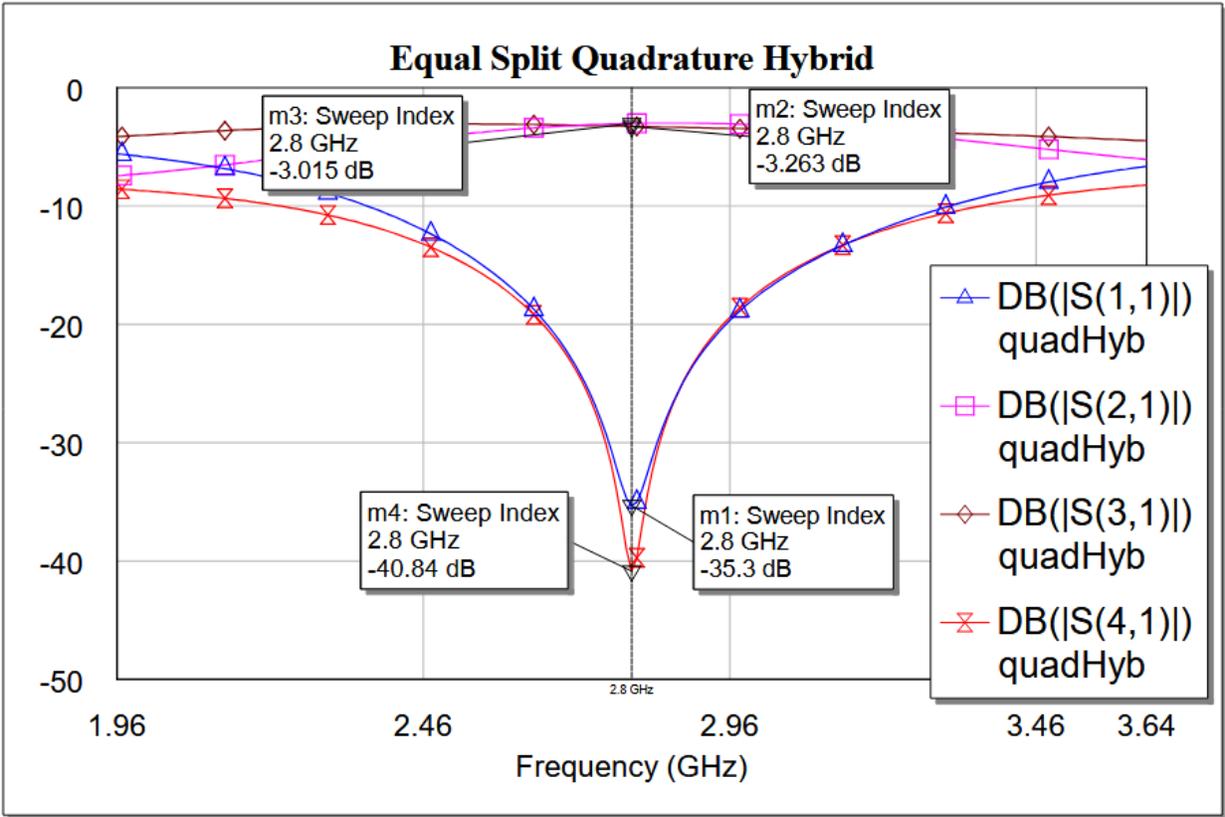
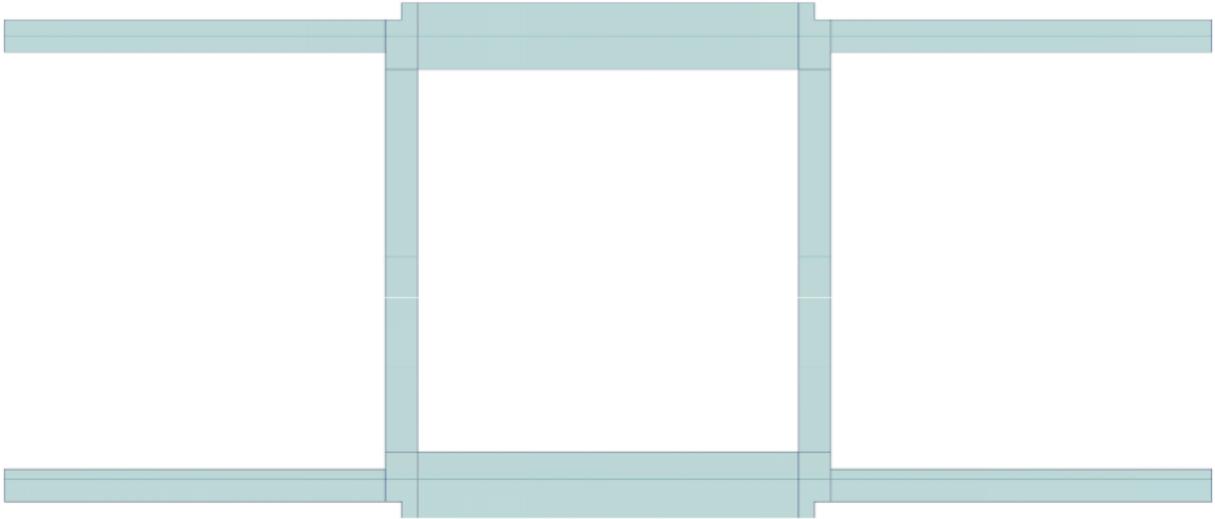


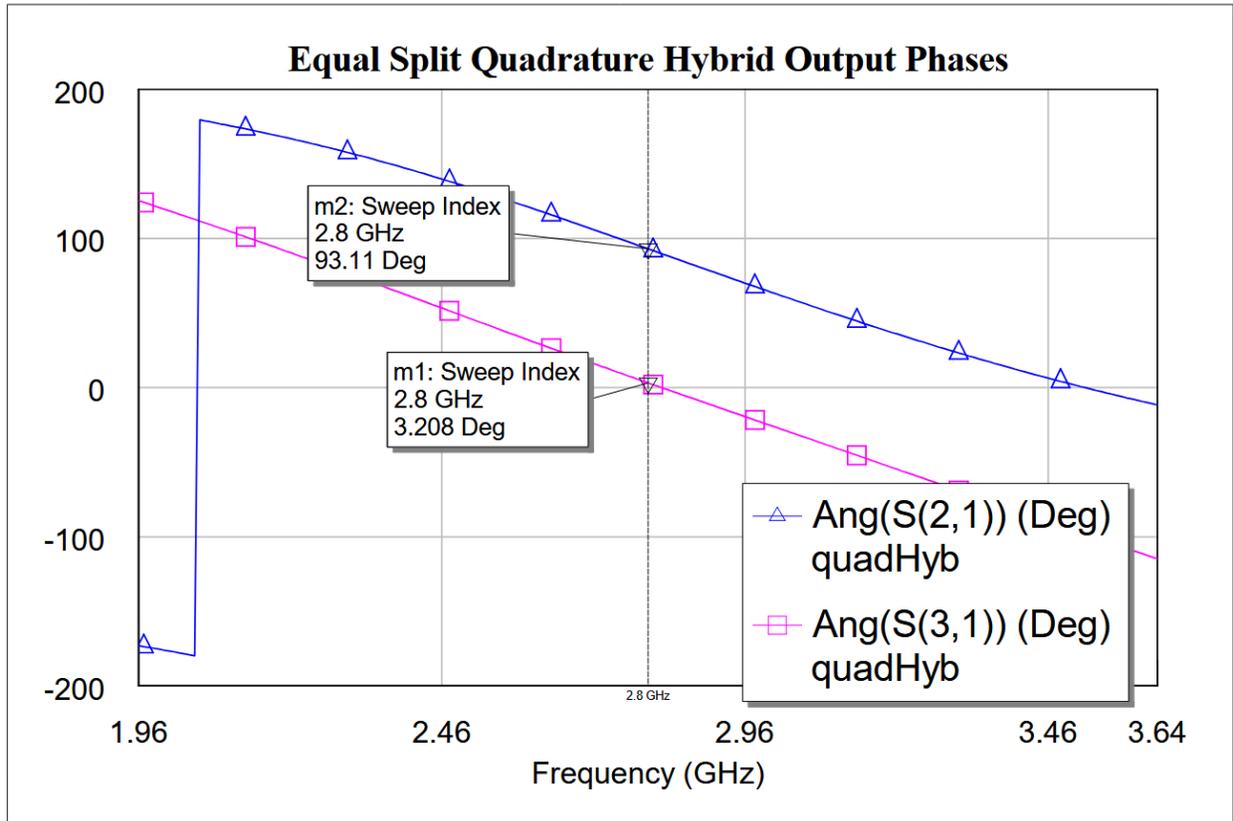


Quadrature Hybrid:

Initial lengths calculated by TXLine were pretty close to the desired performance, but, since T-junctions add some unideal behaviors, with some slight tuning the measurements looked significantly more favorable.







Total 3-Way Equal-Split Power Divider:

I had to add some extra transmission lines and bends to make the connections work correctly, and for the outputs to face the same way and be at about the same plane. Thankfully, they do not seem to have much effect. The total power output is decently evenly split amongst the three outputs!

