

Jason Bissias

Email: bissiasjason@gmail.com || jm@jbissias.xyz

Mobile Telephone: (832) 312-7722

Website: jbissias.xyz

Education:

Texas Tech University; M.S. in Electrical Engineering

Specialization topics: Semiconductor physics, process engineering, device simulation.

January 2025 through May 2026

GPA: 3.656/4.0

Texas Tech University; B.S. in Electrical Engineering, with minor in Computer Science

Specialization topics: VLSI, process engineering, semiconductor physics.

August 2021 through December 2024

GPA: 3.589/4.0

Selected work experience:

Texas Tech University; Graduate Research Assistant

Researched III-V and wide-bandgap semiconductor device physics through physical and simulated measurements under various conditions, with auxiliary responsibilities being in managing lab space and lecturing in semiconductor physics courses.

January 2025 to Present

Texas Tech University; Undergraduate Teaching Assistant

Teaching assistant for semiconductor physics class, including assisting students and giving lectures.

August 2024 through December 2024

Texas Tech University; Undergraduate Research Assistant

Studied and contributed to research on GaN device structures.

January 2024 through May 2024

IT Help Central; Student Assistant

Provided technical assistance for TTU students and faculty.

May 2023 through September 2023

Certifications:

FE in Electrical and Computer Engineering

As of September 2025

Technical skills:

- Semiconductor parameter measurements (I-V, C-V, S-parameters)
- Manual wire bonding (Ball-to-wedge, wedge-to-wedge)
- Electronic device simulators (nextnano, Silvaco TCAD, Cadence Virtuoso, LTspice, Ansys HFSS)
- Computer/chip architectures
- Programming (C/C++, Python, MATLAB, VHDL, Assembly)
- Office productivity suites (Microsoft Office, LibreOffice, Google Workspace)
- Trilingual (English, Greek, French)

Experience in:

- Cryogenic and high-temperature semiconductor characterization
- Management & maintenance of lab environment & equipment
- Server networking
- RF circuit design
- EDA tools
- Lecturing on physics and electronics
- Rapid research and script development
- Technical writing
- Supervision & leadership